6/3/2004

In the Claims:

1. (currently amended) A CDMA decoder for decoding a CDMA encoded signal from a desired CDMA channel, comprising:

a receive input for receiving a CDMA encoded signal;

a code generator for generating a predetermined CDMA code for a predetermined CDMA channel that corresponds to an encoded signal encoded with a corresponding CDMA code transmitted over the predetermined CDMA channel;

a multiply/accumulate device including a multiplication device for multiplying said received signal received on said receive input by said predetermined CDMA code word and operating in the analog domain, said multiply/accumulate device also including an accumulator operable to accumulate the results of the multiplication operation over a symbol period to provide an analog result; ~~and~~

a data conversion device for determining if the analog result corresponds to a predetermined digital state and, if so, generating a digital output corresponding to said predetermine digital state; and

a summation device for subtracting the output of ~~each of~~ said multiplication devices from said analog result of said ~~each multiply/accumulation~~ multiply/accumulate device prior to input to said data conversion device associated with said ~~each~~ multiply/accumulation device.

2/11/2004

In the claims:

Claim 1 (currently amended) A CDMA decoder for decoding a CDMA encoded signal from a desired CDMA channel, comprising:

a receive input for receiving a CDMA encoded signal;

a code generator for generating a predetermined CDMA code for a predetermined CDMA channel that corresponds to an encoded signal encoded with a corresponding CDMA code transmitted over the predetermined CDMA channel;

a multiply or accumulate device for multiplying said received signal received on said receive input by said predetermined CDMA code word and operating in the analog domain, said multiply or accumulate device operable to accumulate the results of the multiplication operation over a symbol period to provide an analog result; ~~and~~

a data conversion device for determining if the analog result corresponds to a predetermined digital state and, if so, generating a digital output corresponding to said predetermined digital state; and

a summation device for subtracting the output of each of said multiplication devices from said analog result of said each multiply or accumulation device prior to input to said data conversion device associated with said each multiply or accumulation device.

Claim 2 (previously presented) The CDMA decoder of Claim 1, wherein said predetermined digital state comprises multiple predetermined digital states and wherein said data conversion device is operable to generate multiple digital output states corresponding to the one of said multiple predetermined digital states to which the analog result has been determined to correspond.

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